

AMENDMENTS TO THE CLAIMS

1. (currently amended) A transponder circuit comprising an input signal section directly coupled to both a first and a second comparator.
2. (original) The circuit of Claim 1, wherein said first comparator consumes less power than said second comparator.
3. (original) The circuit of Claim 1, wherein said second comparator operates at a higher speed than said first comparator.
4. (original) The circuit of Claim 1, wherein said second comparator is only enabled when a predefined signal is detected at said first comparator.
5. (original) The circuit of Claim 1, wherein a reference voltage for said first comparator is adjustable during circuit operation.
6. (original) The circuit of Claim 1, wherein a reference voltage for said second comparator is adjustable during circuit operation.
7. (original) The circuit of Claim 1, further comprising validation circuitry configured to validate said input signal in response to a signal from said first comparator.
8. (original) The circuit of Claim 7, wherein said second comparator is only active when said validation circuitry validates said input signal.
9. (original) The circuit of Claim 1, wherein valid receive signal processing is only performed on a signal produced by said second comparator.
10. (original) The circuit of Claim 1, wherein said second comparator turns off when a voltage level of said input signal is below a voltage level of a reference voltage signal received at said second comparator.
11. (original) A method of reducing power consumption in a transponder circuit, comprising activating a first comparator prior to receiving a predefined signal, and activating a second comparator in response to an output from said first comparator.
12. (original) The method of Claim 11, wherein said first comparator consumes less power than said second comparator.
13. (original) The method of Claim 11, wherein said second comparator operates at a higher speed than said first comparator.

14. (original) The method of Claim 11, further comprising performing transponder signal processing only on a signal produced by said second comparator.

15. (original) A method of operating a transponder circuit, comprising routing an input signal to a first comparator which is enabled and a second comparator which is disabled, and enabling said second comparator in response to a signal output by said first comparator.

16. (original) The method of Claim 15, further comprising operating said second comparator only after a predefined signal received at said first comparator has been validated.

17. (original) The method of Claim 15, wherein said first comparator consumes less power than said second comparator.

18. (original) The method of Claim 15, wherein a reference voltage for said first comparator is equal to a reference voltage for said second comparator.

19. (original) The method of Claim 15, further comprising performing transponder receive signal processing only on a signal received by said second comparator.

20. (original) The method of Claim 15, further comprising activating an oscillator upon detection of a signal at said first comparator, driving a signal validation circuit with a signal from said oscillator, validating said predefined signal from said first comparator at said validation circuit, and activating additional transponder circuit components following signal validation.

21. (original) The method of Claim 15, further comprising adjusting a reference voltage for said first comparator and a reference voltage for said second comparator in response to information received from said input signal.

22. (original) A transponder circuit, comprising:
an input terminal;
a first comparator, coupled to said input terminal;
a second comparator, coupled to said input terminal;
control circuitry, coupled to said first comparator and said second comparator, configured to control the operation of said second comparator by determining a validity status of a signal received from said first comparator.

23. (previously presented) The circuit of Claim 22, wherein said first comparator consumes less power than said second comparator.

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24. (previously presented) The circuit of Claim 22, wherein said second comparator operates at a higher speed than said first comparator.

25. (currently amended) A method of making a transponder circuit, comprising directly coupling an antenna to two separate comparators.

26. (original) The method of Claim 25, wherein one of said two separate comparators consumes less power than the other.

27. (original) The method of Claim 26, further comprising coupling an enable input of one of said comparators to a controller in the transponder circuit.

28. (new) A transponder circuit comprising an input signal section coupled to both a first and a second comparator, wherein said first comparator consumes less power than said second comparator.

29. (new) A transponder circuit comprising an input signal section coupled to both a first and a second comparator, wherein said second comparator operates at a higher speed than said first comparator.

30. (new) A transponder circuit, comprising:

an input terminal;

a first comparator, coupled to said input terminal and configured for operating at a first speed and a first power consumption level;

a second comparator, coupled to said input terminal and configured to operate at a higher speed and a higher power consumption level than said first comparator; and

control circuitry, coupled to said first comparator and said second comparator, and configured to enable said second comparator in response to a signal output by said first comparator.

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SUMMARY OF INTERVIEW

Applicant would like to thank the Examiner for the telephonic interview conducted on June 30, 2005. A summary of the interview appears below.

Exhibits and/or Demonstrations

None

Identification of Claims Discussed

Independent Claims 1, 11, 15, 22, and 25 were discussed.

Identification of Prior Art Discussed

U.S. Patent No. 3,401,340 to Cronburg, et al. and U.S. Patent No. 6,442,378 to Aboukhalil, et al. were discussed.

Proposed Amendments

Applicant did not propose any amendments.

Principal Arguments and Other Matters

Applicant presented arguments directed to the failure of Cronburg and Aboukhalil, either alone or in combination, to teach a transponder circuit comprising an input signal section coupled to both a first and second comparator as recited in Claim 1. Applicant also presented arguments with respect to the failure of Cronburg to teach activating a second comparator in response to an output from a first comparator, and routing an input signal to a second comparator which is disabled, as recited in Claims 11 and 15. Applicant further noted that the application of the Cronburg reference to the claims was inconsistent. Specifically, the Office Action identifies the pilot monitors as anticipating the first and second comparators with regard to Claims 1, 15, 22, and 25, and refers to the comparing means recited at col. 6, ll. 24-36 as anticipating the first and second comparators with regard to Claims 11, 15 and 22.

Results of Interview

No agreement was reached.